Analysis, Design, and Optimization of RF CMOS Polyphase Filters

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Abstract

Polyphase filters (PPFs) are an efficient solution for high accuracy quadrature generation in radio frequency (RF) CMOS design. Although there are some guidelines for design of RF CMOS PPFs, they give too much freedom. With layout considerations, optimization of RF CMOS PPFs cannot be reached by using analytical calculations because of many constraints and tradeoffs in the design. Thus, in design of RF CMOS PPFs, intensive trials and several design iterations are needed to reach given specifications. In this situation, a computer-aided analysis and design optimization tool for RF CMOS PPFs is of great help to the community of RF CMOS PPFs designers, especially for industrial projects that are under time-to-market pressures. However, such a dedicated tool has not been available until now, to the author's best knowledge. Focusing on computer-aided analysis and design optimization tools for RF CMOS PPFs, this dissertation has the following scientific contributions:

- PPFANA, a dedicated computer-aided software tool for analysis of the influences from the nonideal effects, namely, process tolerance, components mismatch on the quadrature accuracy of the RF CMOS PPFs, was developed by the author and presented in this dissertation.
- Physical design considerations for RF CMOS PPFs in system-on-a-chip (SoC) solution were proposed by the author through the analysis on the influences from parasitic effects in RF SoC.
- PPFOPTIMA, a computer-aided synthesis and optimization tool for RF CMOS
 PPFs with dedicated worst-case simulation and Monte Carlo simulation
 toolboxes for verification of the performance of RF CMOS PPFs, was
 developed by the author and presented in this dissertation.

Zusammenfassung

Polyphasenfilter (PPF) sind eine leistungsfähige Lösung für die hochgenaue Quadraturerzeugung im Hochfrequenz (HF)-CMOS-Entwurf. Obgleich es einige Richtlinien für den Entwurf von HF-CMOS-PPF gibt, erlauben diese zu viele Freiheiten. Eine Optimierung der Schaltung und der Layout kann mit Rücksicht auf viele technisch und wirtschaftlich vorgegebene Randbedingungen und der daraus resultierenden Kompromisse mit analytischen Methoden alleine nicht erreichen werden. Es werden aufwändige Versuche und mehrere Entwurfsverbesserungen benötigt, um einen zufriedenstellenden Entwurf zu erreichen. Deshalb ist ein computerbasiertes Werkzeug zur Analyse und Optimierung des Entwurfs von HF-CMOS-PPF besonders für den industriellen Anwendungsbereich wünschenswert. Nach bestem Wissen des Autors gab es bislang ein solches Werkzeug nicht. Im Hinblick auf computerbasierte Werkzeuge zur Analyse und Optimierung des Entwurfs von HF-CMOS-PPF bringt die vorliegende Dissertation folgende wissenschaftliche Beiträge:

- PPFANA, ein spezielles Entwurfswerkzeug für die Simulation der Einflüsse von den nichtidealen Effekten wie beispielsweise von Prozesstoleranzen, von Fehlanpassungen der Komponenten und der Quadraturgenauigkeit, wurde vom Autor entwickelt und vorgestellt.
- Richtlinien für den Entwurf von HF-CMOS-PPF auf "System-on-a-Chip"-Lösungen werden vom Autor unter Berücksichtigung parasitärer Effekte erarbeitet und vorgeschlagen.
- Die erfolgreiche Realisierung eines HF-CMOS-PPF f
 ür einen Bluetooth-Empfänger mit niedriger Zwischenfrequenz wurde mit dem Fraunhofer-IMS 0.6 μm CMOS Prozeß nachgewiesen.
- PPFOPTIMA, ein Synthese- und Optimierungswerkzeug für HF-CMOS-PPF auf Basis von Worst-Case- und Monte-Carlo-Simulationen wurde vom Autor entwickelt und vorgestellt.

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List of Abbreviations

ABBREVIATION FULL EXPRESSION

AM Amplitude mismatch

BALUN Balance unbalance converter

BiCMOS Bipolar CMOS

C Capacitor

CDMA Code division multiple access

CEC Compression and error correction

CM Component mismatch

CMOS Complementary metal oxide semiconductor

CPU Central processing unit

DC Direct current

DCR Direct-down conversion receiver

DECT Digitally enhanced cordless telephony

ESD Electro static discharge

FM Frequency modulation

GA Genetic algorithms
GaAs Gallium arsenide

GM Gain mismatch

GSM Global system for mobile communications

GUI Graphical user interface

IC Integrated circuit

IF Intermediate frequency

IMS Institut für Mikroelektronische Schaltungen

und Systeme

I/O Input / output

I/Q In phase/quadrature phase

IRF Image reject filter
IRM Image reject mixer

IRR Image reject ratio

ISM Industrial, scientific and medical

LNA Low noise amplifier

LO Local oscillator

LPF Low pass filter

MC Monte Carlo

MIM Metal insulator metal

MLR Maximum length of resistors

NF Noise figure

PA Power amplifier

PC Personal computer

PCB Printed circuit board

PD Phase difference

PDF Probability density function

PM Phase mismatching

PPF Polyphase filter

PPFANA Polyphase filter analysis toolbox

PPFN Polyphase filter network

PPFOPTIMA Polyphase filter optimization toolbox

PS Postlayout simulation

PT Process tolerances

OAM Ouadrature amplitude modulation

QM Quadrature modulation

QPSK Quadrature phase shift keying

RC Resistance capacitance

RF Radio frequency
SA Simulated annealing
SiGe Silicon germanium
SoC System on a chip

VCO Voltage controlled oscillator
WLAN Wireless local area network

WCDMA Wideband code division multiple access

List of Symbols

$A_{ m I}$	Amplitude of the I path output signal in RF CMOS PPFs
A_{Q}	Amplitude of the Q path output signal in RF CMOS PPFs
$A_{ m R_e}$	Fitting parameter for the standard deviation of $\sigma_{\rm R_e}$
$A_{\!_{ m R}_{ m sh}}$	Fitting parameter for the standard deviation of $\sigma_{R_{sh}}$
$A_{\Delta \mathrm{w}}$	Fitting parameter for the standard deviation of $\sigma_{\scriptscriptstyle \Delta w}$
$C_{\rm c}$	Parasitic capacitance between back plate of the capacitor and the substrate
C_i	Capacitor of the $i_{\rm th}$ stage in the RF CMOS PPFs
$C_{i,\mathrm{CM}}$	Capacitor of the $i_{\rm th}$ stage with component mismatch
	between I path and Q path in the RF CMOS PPFs
$C_{i,\mathrm{PT}}$	Capacitor of the i_{th} stage with process tolerance
	in the RF CMOS PPFs
$C_{{ m I},i}$	Capacitor of the $i_{\rm th}$ stage in the I path of the RF CMOS PPFs
$C_{ m para}$	Parasitic capacitance
$C_{\mathrm{Q},i}$	Capacitor of the i_{th} stage in the Q path of
	the RF CMOS PPFs
$C_{\mathtt{R}}$	Parasitic capacitance between polysilicon resistor
	and the substrate
C_{sum}	Sum of parasitic capacitance between RF CMOS PPFs
	and the substrate
C_{uaC}	Parasitic capacitance between unit area capacitor
	and the substrate
$C_{\mathrm{uaC}i}$	Parasitic capacitance between the capacitors in the $i_{\rm th}$ stage
	and the substrate
$C_{\mathrm{uaR}i}$	Parasitic capacitance between the resistors in the $i_{\rm th}$ stage
	and the substrate

 C_{unit} Unit capacitance of the capacitor

 d_{phase} Phase difference between I path and Q path in PPFs

f Frequency of a signal

Constant describing influences of area on mismatch

 f_n Constant describing influences of periphery on mismatch

 f_d Constant describing influences of distance on mismatch

 F_i Fitness of the individual i

 $H(j\omega)$ Transfer function of polyphase filters

 $H_1(j\omega)$ Imaginary part of the transfer function of polyphase filters

 $H_{\rm p}(j\omega)$ Real part of the transfer function of polyphase filters

Input current in RF CMOS PPFs

 $I_{k,in}$ The k_{th} input current in RF CMOS PPFs

 $I_{k,out}$ The k_{th} output current in RF CMOS PPFs

I_{out} Output current in RF CMOS PPFs

 $L_{\text{max Res}}$ Maximum length of the resistor

 L_{R} Length of the resistor

 M_C Mismatch between the neighboring capacitors

 $M_{\rm CMC}$ Component mismatch of the capacitors

 M_{CMR} Component mismatch of the resistors

 $M_{\rm R}$ Mismatch between the neighboring resistors

 $m_{\rm gain,CM}$ Gain mismatch in RF CMOS PPFs

with component mismatch

 $m_{\rm gain,ideal}$ Gain mismatch in the ideal RF CMOS PPFs

 $m_{\text{gain,PT}}$ Gain mismatch in RF CMOS PPFs

with process tolerances

 $m_{\rm nhase}$ Phase mismatch in RF CMOS PPFs

 $m_{\rm phase,CM}$ Phase mismatch in RF CMOS PPFs

with component mismatch

 $m_{\text{phase ideal}}$ Phase mismatch in the ideal RF CMOS PPFs

 $m_{\rm phase,PT}$ Phase mismatch in RF CMOS PPFs

with process tolerances

 $N_{\rm unit}$ Number of unit capacitor

 P_i Probability of selection of the j population

 $P_{\rm p}$ Process tolerance of the resistors

Q Quality factor

 R_{a} End resistance of a resistor

 R_i Resistor of the i_{th} stage in the RF CMOS PPFs

 $R_{i,\text{CM}}$ Resistor of the i_{th} stage with component mismatch

between I path and Q path in the RF CMOS PPFs

 R_{Li} Resistors of the i_{th} stage in the I path of the RF CMOS PPFs

 $R_{i,PT}$ Resistor of the i_{th} stage with process tolerance

in the RF CMOS PPFs

 R_{IR} Image reject ratio

 $R_{O,i}$ Resistors of the i_{th} stage in the Q path of the RF CMOS PPFs

 $R_{\rm sh}$ Sheet resistance of resistor $R_{\rm sub}$ Resistivity of the substrate

 R_{LIIR} Ultimately obtainable image reject ratio

 S_{Ci} Area of the capacitor in the i_{th} stage in RF CMOS PPFs

 S_{Ri} Area of the resistor in the i_{th} stage in RF CMOS PPFs

 $S_{\rm R}$ Area of a resistor

 V_{in} Input voltage in RF CMOS PPFs

 $V_{k, \text{ in}}$ The k_{th} input voltage in RF CMOS PPFs

 $V_{k, \text{out}}$ The k_{th} output voltage in RF CMOS PPFs

 $V_{\text{L.in}}$ Real part of the input signal in RF CMOS PPFs

 $V_{\rm I.\,mid}$ Intermediate I path signal

 $V_{O \text{ mid}}$ Intermediate Q path signal

 V_{tout} I path output signal in RF CMOS PPFs

 V_{out} Output voltage in RF CMOS PPFs

 $V_{O \text{ in}}$ Imaginary part of the input signal in RF CMOS PPFs

 V_{Ocout} Q path output signal in RF CMOS PPFs

 $W_{\rm R}$ Width of a resistor

 w_i Weight of the i_{th} item

Y Admittances

 ω Angular frequency

 ω_{IF} Angular frequency of the intermediate frequency signal

 ω_{image} Angular frequency of the image

 ω_{10} Angular frequency of the signal from the local oscillator

 ω_{RF} Angular frequency of the radio frequency signal

 $\omega_{ ext{ iny cional}}$ Angular frequency of the signal

 σ_{a} Standard deviation of mismatch caused by area variation

 $\sigma_{\mbox{\tiny component}}$ Standard deviation of component mismatch

 $\sigma_{\rm d}$ Standard deviation of mismatch caused by distance $\sigma_{\rm p}$ Standard deviation of mismatch caused by periphery

 $\sigma_{\rm Re}$ Standard deviation of the end resistance

 $\sigma_{R_{sh}}$ Standard deviation of the sheet resistance

 $\sigma_{\Delta w}$ Standard deviation of the width of a resistor

 ϕ Phase of a signal

 θ Relative phase difference in RF CMOS PPFs

 ΔA Amplitude mismatch in volts $\Delta \phi$ Phase mismatch in degrees